

**AMENDMENTS TO THE CLAIMS:**

Please amend claims 1, 3, 4, 6, 7, 9, and 11, as indicated in the following listing of claims, which replaces all prior versions and listings of claims in the application:

1. (Currently Amended) An electronic device comprising:  
  
means for inhibiting activation of the electronic device;  
  
means for holding information representing whether activation of the electronic device is inhibited by the inhibiting means; and  
  
means for executing activation or activation inhibition of the electronic device on the basis of the information stored in the ~~storage~~ holding means when an activation instruction is generated in the electronic device.
2. (Original) A device according to claim 1, wherein the inhibiting means is arranged in a housing of the electronic device.
3. (Currently Amended) A device according to claim 1, wherein the ~~control~~ executing means includes a sub-processor different from a main processor arranged in the electronic device.
4. (Currently Amended) A device according to claim 1, wherein the ~~control~~ executing means includes a sub-processor different from a main processor arranged in the electronic device, and the ~~storage~~ holding means is arranged in the sub-processor.

5. (Original) A device according to claim 2, wherein the inhibiting means designates inhibition even when the electronic device is OFF.

6. (Currently Amended) A device according to claim 3, which further comprises a power supply controller that manages a power supply of the electronic device, and in which the ~~control~~ executing means issues a power supply request to the power supply controller when the activation instruction is received and activation of the electronic device is permitted.

7. (Currently Amended) An electronic device comprising:  
means for inhibiting activation of the electronic device; and  
means for, when an activation instruction is generated from software which runs in the electronic device, deciding whether activation of the electronic device is inhibited based on information representing whether activation of the electronic device is inhibited by the inhibiting means, and when activation is inhibited, inhibiting activation of the electronic device.

8. (Original) A device according to claim 7, wherein the inhibiting means is arranged in a housing of the electronic device.

9. (Currently Amended) A device according to claim 7, wherein the ~~control~~ deciding means includes a sub-processor different from a main processor arranged in the electronic device.

10. (Original) A device according to claim 8, wherein the inhibiting means designates inhibition even when the electronic device is OFF.

11. (Currently Amended) A device according to claim 7, which further comprises a power supply controller that manages a power supply of the electronic device, and in which the ~~control~~ deciding means issues a power supply request to the power supply controller when the activation instruction is received and activation of the electronic device is permitted.

12. (Original) An electronic device comprising:  
means for inhibiting activation of the electronic device;  
means for holding information representing whether activation is inhibited by the inhibiting means; and  
means for executing activation or activation inhibition of the electronic device on the basis of the information stored in the storage means upon reception of one of an activation instruction from a power supply switch arranged in a housing of the electronic device, an activation instruction from software which runs in the electronic device, and an activation instruction from a network connected to the electronic device.

13. (Original) A power supply control method in an electronic device, comprising:  
storing information representing activation inhibition or activation permission of  
the electronic device that is designated by inhibiting means for inhibiting activation of  
the electronic device;

receiving an activation instruction for the electronic device from software which  
runs in the electronic device; and

inhibiting activation of the electronic device when the stored information  
represents activation inhibition.

14. (Original) A method according to claim 13, wherein storage of the  
information, reception of the activation instruction, and activation inhibition of the  
electronic device are performed by a sub-processor.

15. (Original) A method according to claim 13, wherein  
a power supply controller which manages a power supply of the electronic device  
is further arranged, and

a power supply request is issued to the power supply controller when the  
activation instruction for the electronic device is received and the information permits  
activation.

16. (Original) A power supply control method in an electronic device having inhibiting means for inhibiting activation of the electronic device, and storage means for holding information representing whether activation is inhibited by the inhibiting means, comprising:

receiving an activation instruction from a power supply switch arranged in a housing of the electronic device, an activation instruction from software which runs in the electronic device, or an activation instruction from a network connected to the electronic device; and

executing activation or activation inhibition of the electronic device on the basis of the information stored in the storage means.